Lr No: PACE(A)/ECS/2021-22/101   09/09/2022

CIRCULAR

All the students are hereby informed that the M.Tech II Semester (R21) Regular (2021 Admitted Batch) examinations will commence from **26/09/2022.** The Time Table for these examinations is given below.

	Timing of the Semester End Examination: 10:00 am to 1:00 pm		
Date	CE (SE)	ECE(VLSI &ES)	CSE (CSE)
26.09.2022 (Mon Day)	Finite Element Method in Structural Engineering	CMOS Analog and Digital IC Design	Data Science Applications with Python
28.09.2022 (Wed Day)	Theory of Plates and Shells	Real Time Operating Systems	Mean Stack Technologies
30.09.2022 (Fri Day)	Advanced Steel Design	Network Security & Cryptography	Internet of Things
06.10.2022 (Thurs Day)	Earthquake Resistant Design of Buildings	Low Power VLSI Design	Cloud Computing

NOTE:
i) Any Omissions or clashes in this Time Table may please be informed to the Controller of Examinations immediately
ii) Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual

CONTROLLER OF EXAMINATIONS

PRINCIPAL

PRINCIPAL
PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)
VALLUR, ONGOLE-523 272, ANDHRA PRADESH.

Copy to the
: CE,ECE,CSE HoD's for circulation to the Staff & Students
: Director, IQAC- for information   : Dean, Academics – for information
: Notice board at Exam Cell & System   : Office File